

In Place of FORM PTO-1449 (Modified)

Serial No. 10/682,413

Applicant: Keohane et al.

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT**

Filing Date: \_\_\_\_\_

Group: \_\_\_\_\_

Atty. Docket No. AUS920030526US1

Reference Designation			U. S. PATENT DOCUMENTS			Filing Date if Appropriate
Examiner Initial	Document Number	Date	Name	Class	Subclass	
<u>HV</u>	AA 6,557,079 B1	4/29/2003	Mason, Jr. et al.	711	137	12/20/1999
	AB 6,453,396 B1	9/17/2002	Boone et al.	711	162	7/14/1999
	AC 6,256,705 B1	7/3/2001	Li et al.	711	112	6/5/1998
	AD 6,253,209 B1	6/26/2001	Chase-Salerno et al.	707	104	6/7/1998
	AE 6,223,206 B1	4/24/2001	Dan et al.	709	105	8/28/1998
	AF 6,112,257	8/29/2000	Mason, Jr. et al.	710	18	9/24/1997
	AL 6,044,367	3/28/2000	Wolff	707	2	4/15/1998
	AM 6,032,216	2/29/2000	Schmuck et al.	710	200	7/11/1997
	AN 5,951,694	9/14/1999	Choquier et al.	714	15	2/3/1997
	AO 5,802,394	9/1/1998	Baird et al.	395	825	6/6/1994
<u>✓</u>	AP 5,802,301	9/1/1998	Dan et al.	395	200	5/11/1994

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
	AR					
	AS					

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner Initial	
<u>HV</u>	AT <u>Optimized Process For Balancing Load For Data Mirroring</u> , by Burkes et al., Patent Application Serial No. 10/189,270 filed on 7/3/2002
<u>HV</u>	AU <u>A Performance Evaluation of RAID Architectures</u> , by Shenze Chen and Don Towsley, IEEE Transactions on Computers, Vol. 45, No. 10 October 1996
	AV

Examiner: Hiep T. Nguyen

Date Considered: 10/2/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.